ENH064V1-800 Color TFT-LCD Module Features

GENERAL DESCRIPTION

WEDC provides optically enhanced solutions to the standard Sharp LQ64D343 color active matrix LCD module. The ENH064V1-800 enhancement is an index matching (IM) film lamination to the front surface of the display polarizer. The IM film is available in two surface treatments - IM/Clear and IM/110 (a 10% diffusion). The second enhancement is the incorporation of an enhanced light guide (ELG) providing for up to 30% increase in brightness.

This module is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a 640~x RGB x 480 dot panel with 262,144 colors by supplying 18-bit data signal (6-bit/color), four timing signals, +5V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module is a low-reflection and higher-color-saturation type. Therefore, this module is also suitable for multimedia use. Viewing angle is 6 o'clock direction. This module offers a wide viewing angle and high brightness (800cd/m²). Backlight-driving DC/AC inverter is not built in this module.

WEDC assumes no responsibility for any damage resulting from the use of the device which does not comply with the instructions and the precautions specified in these specification sheets. WEDC does assume the responsibility for the warranty of the enhanced product.

MECHANICAL SPECIFICATIONS

Parameter	Specifications	Unit
Display Size	16 (6.4") Diagonal	cm
Active Area	130.6(H) x 97.0(V)	mm
Pixel Format	640(H) x 480(V) (1 pixel=R+G+B dots)	pixel
Pixel Pitch	0.204(H) x 0.202(V)	mm
Pixel Configuration	RGB vertical stripe	-
Display Mode	Normally white	-
Unit Outline Dimensions (1)	175.0(W) x 126.5(H) x 16.0(D)	mm
Mass	290 ± 20	g
Surface Treatment	IM/Clear (glossy) or IM/110 and hardcoat 3H	-

Note:

1. Excluding backlight cables.

INPUT TERMINALS

TFT-LCD panel driving

CN1 Used connector:
Corresponding connector:

DF9BA-31P-1V (Hirose Electric Co., Ltd.)
DF9-31S-1V (Hirose Electric Co., Ltd.)

DF9A-31S-1V (Hirose Electric Co., Ltd.) DF9B-31S-1V (Hirose Electric Co., Ltd.)

DF9M-31S-1V (Hirose Electric Co., Ltd.)

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(Transparent view)

Pin No.	Symbol	Function	Remark
1	GND	_	_
2	CK	Clock signal for sampling each data signal	_
3	Hsync	Horizontal synchronous signal	(1)
4	Vsync	Vertical synchronous signal	(1)
5	GND	-	-
6	R0	RED data signal (LSB)	_
7	R1	RED data signal	_
8	R2	RED data signal	_
9	R3	RED data signal	_
10	R4	RED data signal	-
11	R5	RED data signal (MSB)	-
12	GND	-	_
13	G0	GREEN data signal (LSB)	_
14	G1	GREEN data signal	_
15	G2	GREEN data signal	_
16	G3	GREEN data signal	_
17	G4	GREEN data signal	_
18	G5	GREEN data signal (MSB)	_
19	GND	-	_
20	В0	BLUE data signal (LSB)	_
21	B1	BLUE data signal	_
22	B2	BLUE data signal	_
23	B3	BLUE data signal	_
24	B4	BLUE data signal	_
25	B5	BLUE data signal (MSB)	_
26	GND	-	_
27	ENAB	Signal to settle the horizontal display position	(2)
28	Vcc	+5.0V power supply	_
29	Vcc	+5.0V power supply	_
30	R/L	Horizontal display mode select signal	(3)
31	U/D	Vertical display mode select signal	(4)

The shielding case is not connected with GND.

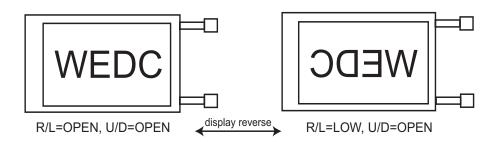
Notes

- 480 line, 400 line or 350 line mode is selected by the polarity combination of both synchronous signals.
- 2) The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in Horizontal Display Position, p. 6. Do not keep ENAB "High" during operation.

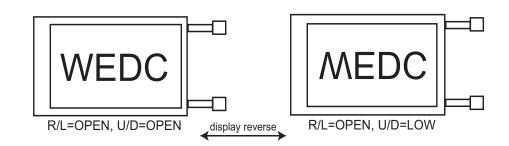
Mode	480 lines	400 lines	350 lines
Hsync	negative	negative	positive
Vsync	negative	positive	negative

Original specifications created by Sharp.





4)



Used connector: BHR-03VS-1(JST)
Corresponding connector: SM02(8.0)B-BHS(JST)

BACKLIGHT DRIVING

CNA, CNB

Pin No.	Symbol	Function
1	VHIGH	Power supply for lamp (High voltage side)
2	NC	This is electrically opened
3	V _{LOW}	Power supply for lamp (Low voltage side)

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input Voltage	Vı	Ta=25°C	-0.3~ Vcc + 0.3	V	(1)
+5V Supply Voltage	Vcc	Ta=25°C	0~+6	V	_
Storage Temperature	Tstg	_	-25 ~ + 70	°C	(2)
Operating Temperature (Ambient)	Тора	-	0 ~ + 55	°C	

Notes:

- 1. CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D
- Humidity: 95%RH Max. at Ta ≤ 40°C.
 Maximum wet-bulb temperature at 39°C or less at Ta>40°C.

 No condensation.

ELECTRICAL CHARACTERISTICS

TFT-LCD Panel Driving

Paran	neter	Symbol	Min.	Тур.	Max.	Unit	Remark
+5V	Supply Voltage	Vcc	+4.5	+5.0	+5.5	V	(1)
737	Current Dissipation	Icc	_	250	360	mA	(2)
Permissive Input Ripple Voltage		Vrf	_	-	100	mVp-p	Vcc=+5V
Input Voltage (Low)		VIL	_	-	0.3Vcc	V	(2)
Input '	Voltage (High)	Vih	0.7Vcc	-	_	V	(3)
Innut	Current (Lour)	l _{OL1}	_	-	1.0	μA	V _I =0V (4)
Input	Current (Low)	l _{OL2}	_	-	60.0	μA	V _I =0V (5)
Innut	Oursent (Llich)	Іон1	_	-	1.0	μA	V _I =V _{CC} (6)
Input	Current (High)	Іон2	_	-	60.0	μA	V _I =V _{CC} (7)

Notes:

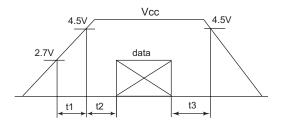
1) Vcc-turn-on conditionst1 ≤ 10ms0 < t2 ≤ 10ms

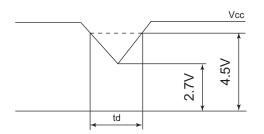
0 < t2 ≤ 1011 0 < t3 ≤ 1s

Vcc-dip conditions 1) $2.7V \le V_{CC} < 4.5V$ $td \le 10ms$

2) Vcc < 2.7V

Vcc-dip conditions should also follow the Vcc-turn-on conditions

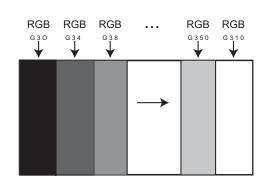




Notes:

2) Typical current situation : 16-gray-bar pattern 480 line mode V_{CC} = +50V

- 3) CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D
- 4) CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB
- 5) R/L
- 6) CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync
- 7) ENAB, U/D



Original specifications created by Sharp.

TA=25°C

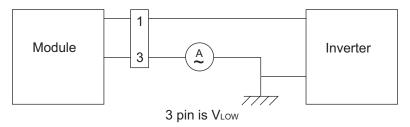
BACKLIGHT DRIVING

The backlight system is an edge-lighting type with four CCFTs (Cold Cathode Fluorescent Tube). The Characteristics of a single lamp are shown in the following table.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp Current	I _L	2.0	6.0	6.5	mArms	(1)
Lamp Power Consumption	PL	_	2.16	_	W	(2)
Lamp Frequency	FL	20	35	60	KHz	(3)
Kick-off Voltage	Vs	_	_	800	Vrms	Ta=25°C
		_	_	1000	Vrms	Ta=0°C (4)
Lamp Life Time	LL	_	25000	-	hour	(5)

Notes:

1) Lamp current is measured with current meter for high frequency as shown below.



- 2) At the condition of Y_L=(300) cd/m²
- 3) Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- The open output voltage of the inverter shall be maintained for more than 1 sec; otherwise the lamp may not be turned on.
- Since lamp is consumable, the life time written above is referential value and is not guaranteed in this specification sheet by WEDC.
 - Lamp life time is defined that it applied either (1) or (2) under this condition (Continuous turning on at Ta=25°C, IL=6mArms)
 - 1) Brightness becomes 50% of the original value under standard condition.
 - 2) Kickoff voltage at Ta=0°C exceeds maximum value, 1000 Vrms. If operating under lower temp environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating for around 1 month under lower temp condition may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp environment, periodic lamp exchange by WEDC is recommended.

Note:

1) The performance of the backlight, for example life time or brightness, is influenced by the characteristics of the DC /AC inverter for the lamp. When designing or ordering the inverter, make certain that poor lighting caused by the mismatch of the backlight and the inverter (mis-lighting, flicker, etc.) do not occur. Once this is confirmed, the module should be operated in the same condition as it is installed in the instrument.

TIMING CHARACTERISTICS OF INPUT SIGNALS

Timing diagrams of input signal are shown in Fig. 2-1~3.

TIMING CHARACTERISTICS

Parameter		Symbol	Mode	Min.	Тур.	Max.	Unit
	Frequency	1/Tc	all	-	25.18	28.33	MHz
Clock	High Time	Tch	1	5	-	-	
	Low Time	Tcl	1	10	-	-	ns
Data	Setup Time	Tds	1	5	-	-	ns
Data	Hold Time	Tdh	1	10	-	-	ns
	Cuele	TH	1	30.00	31.78	-	μs
Horizontal sync. Signal	Cycle	IП	1	770	800	900	ns ns ns ns ps clock clock line line line clock
	Pulse Width	ТНр	1	2	96	200	
			480	515	525	560	line
\/til	Cycle	TV	400	446	449	480	line
Vertical sync. Signal			350	447	449	510	line
	Pulse Width	TVp	all	2	-	34	line
Horizontal Display Period	t	THd	1	640	640	640	clock
Hsync-Clock Phase Diffe	erence	THc	1	10	-	Tc-10	ns
Hsync-Vsync Phase Diffe	erence	TVh	1	0	-	TH-THp	clock

Note:

HORIZONTAL DISPLAY POSITION

The horizontal display position is determined by ENAB signal and the input data corresponding to the rising edge of ENAB signal is displayed at the left end of the active area.

Parameter		Symbol	Min.	Тур.	Max.	Unit
Enable Cianal	Setup time	Tes	5	_	Tc-10	ns
Enable Signal	Pulse width	Тер	2	640	640	clock
Hsync-Enable S	Signal Phase Difference	THe	44	-	TH-664	clock

Note:

¹⁾ In case of lower frequency, the deterioration of display quality, flicker, etc., may occur.

When ENAB is fixed "Low", the display starts from the data of C104 (clock) as shown in Fig. 2-1~3. Be careful the module does <u>not</u> work when ENAB is fixed "High".



VERTICAL DISPLAY POSITION

The vertical display position is automatically centered in the active area at each mode of VGA, 480-, 400-, and 350-line mode. Each mode is selected depending on the polarity of the synchronous signals described in Note 1 on page 2.

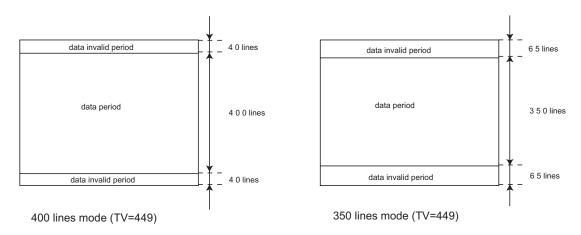
In each mode, the data of TVn is displayed on the top line of the active area. The display position will be centered

on the screen like the following figure when the period of vertical synchronous signal, TV, is typical value.

In 400-, and 350-line mode, the data in the vertical data invalid period is also displayed. So inputting all data "()" is recommended during vertical data invalid period.

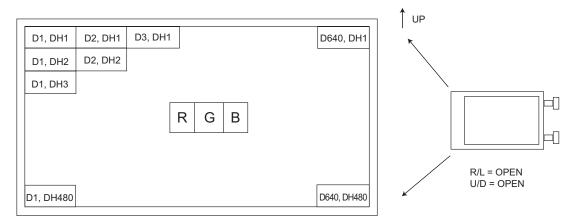
ENAB signal has no relation to the vertical display position.

Mode	V-data Start (TVs)	V-data Period (TVd)	V-display Start (TVn)	V-display Period	Unit
480	34	480	34	480	line
400	34	400	443-TV	480	line
350	61	350	445-TV	480	line



INPUT DATA SIGNALS AND DISPLAY POSITION ON THE SCREEN

Display postion of input data (480 lines mode)



Original specifications created by Sharp.

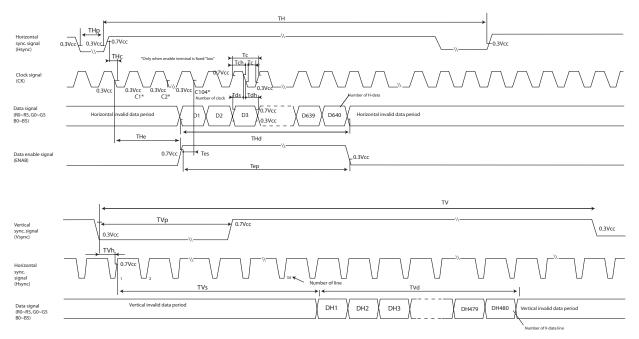


Fig 2-1 Input signal waveforms (480 line mode)

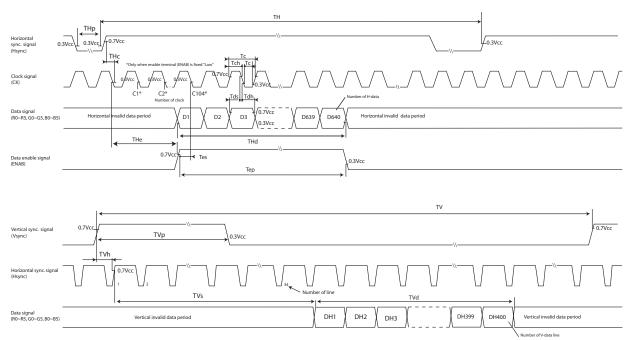
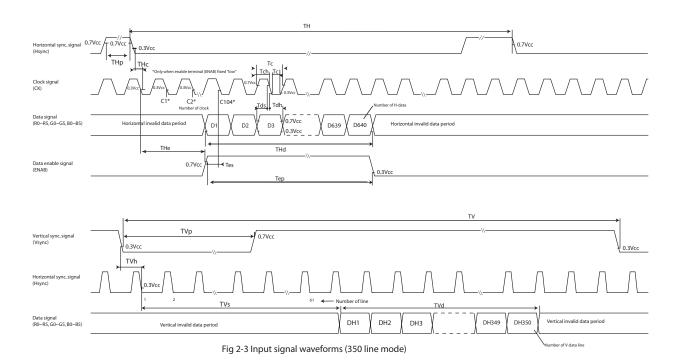


Fig. 2-2 Input signal waveforms (400 line mode)



INPUT SIGNALS, BASIC DISPLAY COLOR AND GRAY SCALE OF EACH COLOR

	Colors &									Data	Signa	al								
	ray scale	Gray Scale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	В1	B2	В3	В4	B5
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
₅	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
8	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Basic Color	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
l m	Magenta	-	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u>0</u>	†	GS1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale or Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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ray	Brighter	GS61	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
٥	<u></u>	GS62	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
en	 	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
Gre	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
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Scal	Doi: July 1	↓			,	ļ					,	ļ					,	ļ		
Gray Scale of Green	Brighter ↓	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
ō	<u> </u>	GS6	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e e	 	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
B	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Gray Scale of Blue	1	<u></u>		<u> </u>						l						Į				
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ıray	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
"		GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Notes:

^{1) 0:} Low level voltage 1: High level voltage

²⁾ Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

OPTICAL CHARACTERISTICS

Ta=25°C, Vcc=+5V

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	θ 21	(CR≥5)	60	70	-	Deg.	(4)
		θ 22		60	70	-	Deg.	
	Vertical	θ 11		35	40	_	Deg.	
		θ 12		55	70	_	Deg.	
Contract Datie		CR	Best Viewing	100	_	_	_	(2.4)
Contrast Ratio		CR	Angle					(2,4)
Response Time	Rise	tr		-	20	-	ms	(3,4)
	Decay	td		-	40	-	ms	
Chromaticity of White		Х	θ = 0°	-	0.313	-	_	(4)
		Y		-	0.325	-	_	
Luminance of White		YL		700	800	_	cd/m ²	
White Uniformity		δw		_	_	1.45	_	(5)
Viewing Angle Range as a Brightness Definition	Horizontal	θ 21	50% of the maximum brightness	_	45	_	Deg.	(1)
		θ 22		_	45	_	Deg.	
	Vertical	θ 11		_	45	_	Deg.	
		θ 12		_	45	_	Deg.	

Note:

¹⁾ The measurement shall be executed 30 minutes after lighting at rating. (typical condition: IL=6mArms) The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig. 3 below.

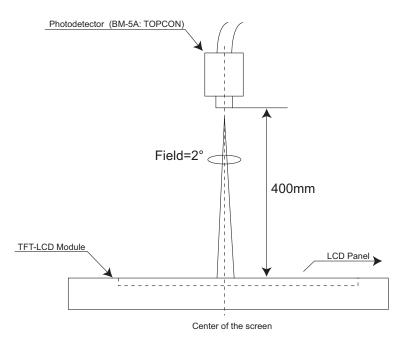
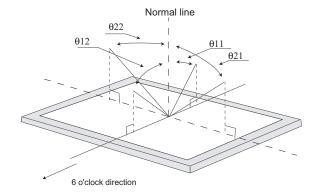


Fig. 3 Optical Characteristics Measurement Method

Notes:

1) Definitions of viewing angle range:

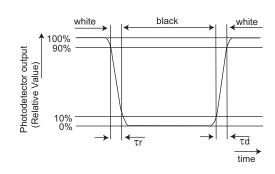


Definition of contrast ratio:
 The contract ratio is defined as the following.

 $Contrast \ Ratio \ (CR) = \frac{Luminance \ (brightness) \ with \ all \ pixels \ white}{Luminance \ (brightness) \ with \ all \ pixels \ black}$

3) Definition of response time:

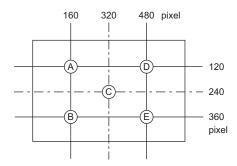
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



- 4) This shall be measured at center of the screen.
- 5) Definition of white uniformity:
 White uniformity is defined as the following with five measurements.

(A~E).

 $\delta_{\textit{W}} = \frac{\textit{Maximum Luminance of five points (brightness)}}{\textit{Minimum Luminance of five points (brightness)}}$



DISPLAY QUALITY

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

HANDLING PRECAUTIONS

- 1) Be sure to turn off the power supply when inserting or disconnecting the cable.
- Design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- Since the front polarizer is easily damaged, pay attention to avoid rubbing with something hard or sharp.
- Wipe water off immediately. Long contact with water may cause discoloration or spots.
- When the panel surface is soiled, use an absorbent cotton or other soft cloth to wipe it off.
- 6) Since the panel is made of glass, refined wires and components, it may break, crack or cause internal wire breaking if dropped or bumped on hard surface. Handle with care.
- 7) Since CMOS LSI is used in this module, make certain one is grounded when handling.
- 8) Observe all other precautionary requirements in handling components.
- This module has its circuitry PCBs on the rear side and should be carefully handled in order to avoid being stressed.

- 10) Laminated film is attached to the module surface to prevent it from being scratched. Before use, peel the film off slowly, with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off 'dust' on the polarizer by using an ionized nitrogen gun.
- Connect GND to 4 place of mounting holes to stabilize against EMI and external noise.
- The high voltage portions on the backlight are very dangerous. Careless handling may lead to electrical shock.

PACKING FORM

- 1) Piling number of cartons: MAX 10
- 2) Package quantity in one carton: 20pcs
- 3) Carton size: 355 (W) X 282 (D) X 265 (H) mm
- 4) Total mass of 1 carton filled with full modules: 7.0kg

OTHERS

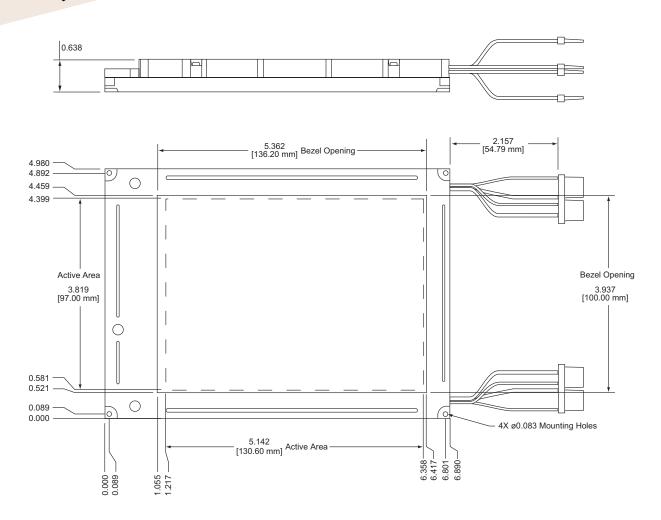
- Disassembling the module can cause permanent damage and should be avoided.
- Image retention may occur when a fixed pattern is displayed for a long time.

EXCHANGE OF LAMPS

This must be action must be performed by WEDC.

RELIABILITY TEST ITEMS

No.	Test item	Conditions	
1	High Temperature Storage Test	Ta=70°C	240h
2	Low Temperature Storage Test	Ta=25°C	240h
3	High Temperature & High Humidity Operation Test	Ta=40°C, 95%RH (No Condensation)	240h
4	High Temperature Operation Test	Ta=55°C	240h
5	Low Temperature Operation Test	Ta=0°C	240h
6	Vibration Test (Non-Operating)	Frequency Sweep time Test period	:10~57Hz/Vibration width (one side):0.075mm :58~500Hz/Gravity: 9.8m/s2 :11 minutes :3 hours (1 hour for each direction of X,Y,Z)
7	Shock Test (Non-Operating)	Max. gravity Pulse width Direction	:490m/s² :11ms, half sine wave :±X, ±Y, ±Z (once for each direction.)



1)	TOLERANCE	X-DIRECTION	A:4×12±0×8
2)	TOLERANCE	X-DIRECTION	B:1×52±0×8
3)	TOLERANCE	Y-DIRECTION	C:1×52±0×8
4)	TOLERANCE	Y-DIRECTION	D:1×52±0×8
5)	OBLIQUITY OF I	DISPLAY AREA	IE-FI<0×8

Notes:

- 1) Unspecified tolerance to be ±0.5.
- 2) Floating of PWB and chassis are excluded from module thickness.
- 3) Diameter of mounting boss shall be the range of 3.5mm to 4mm. (center of oval hole)